



Patent

Attorney's Docket No. 018976-2103723  
#

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )  
)  
Kiyoyuki NAKAGAWA et al. ) Group Art Unit: 3723  
)  
Application No.: 09/988,675 ) Examiner: Lee D. Wilson  
)  
Filed: November 20, 2001 ) Confirmation No.: 9042  
)  
For: ELECTRONIC COMPONENT CHIP )  
FEEDER AND MANUFACTURING )  
METHOD OF ELECTRONIC DEVICES )  
USING ELECTRONIC COMPONENT )  
CHIPS )

RECEIVED

DEC 16 2002

TECHNOLOGY CENTER R3700

AMENDMENT/REPLY TRANSMITTAL LETTER

Assistant Commissioner for Patents  
Washington, D.C. 20231

Date: December 11, 2002

Sir:

Enclosed is a reply for the above-identified patent application.

- ☐ A Petition for Extension of Time is also enclosed.
- ☐ A Terminal Disclaimer and a check for ☐ \$55.00 (2814) ☐ \$110.00 (1814) to cover the requisite Government fee are also enclosed.
- ☐ Also enclosed is \_\_\_\_\_
- ☐ Small entity status is hereby claimed.
- ☐ Applicant(s) request continued examination under 37 C.F.R. § 1.114 and enclose the ☐ \$370.00 (2801) ☐ \$740.00 (1801) fee due under 37 C.F.R. § 1.17(e).
- ☐ Applicant(s) previously submitted \_\_, on \_\_, for which continued examination is requested.
- ☐ Applicant(s) request suspension of action by the Office until at least \_\_, which does not exceed three months from the filing of this RCE, in accordance with 37 C.F.R. § 1.103(c). The required fee under 37 C.F.R. § 1.17(i) is enclosed.
- ☐ A Request for Entry and Consideration of Submission under 37 C.F.R. § 1.129(a) (146/246) is also enclosed.
- ☒ No additional claim fee is required.

(10/02)

☐ An additional claim fee is required, and is calculated as shown below:

AMENDED CLAIMS					
	NO. OF CLAIMS	HIGHEST NO. OF CLAIMS PREVIOUSLY PAID FOR	EXTRA CLAIMS	RATE	ADD'T'L FEE
Total Claims	12	MINUS 20 =	0	× \$18.00 (1202) =	0.00
Independent Claims	2	MINUS 3 =	0	× \$84.00 (1201) =	0.00
If Amendment adds multiple dependent claims, add \$280.00 (1203)					
Total Amendment Fee					
If small entity status is claimed, subtract 50% of Total Amendment Fee					
TOTAL ADDITIONAL FEE DUE FOR THIS AMENDMENT					0.00

☐ A claim fee in the amount of \$\_\_\_\_\_ is enclosed.

☐ Charge \$\_\_\_\_\_ to Deposit Account No. 02-4800.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17, 1.20(d) and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By: \_\_\_\_\_

Ellen Marcie Emas  
Registration No. 32,131

P.O. Box 1404  
Alexandria, Virginia 22313-1404  
(703) 836-6620

Date: December 11, 2002



Patent  
Attorney's Docket No. 018976-210

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )

Kiyoyuki NAKAGAWA et al. )

Application No.: 09/988,675 )

Filed: November 20, 2001 )

For: ELECTRONIC COMPONENT CHIP )  
FEEDER AND MANUFACTURING )  
METHOD OF ELECTRONIC )  
DEVICES USING ELECTRONIC )  
COMPONENT CHIPS )

Group Art Unit: 3723

Examiner: Lee D. Wilson

Confirmation No.: 9042

#9/Conalt  
C

M. White

12/18/02

Handwritten signature and date 12/18/02

RECEIVED

DEC 16 2002

TECHNOLOGY CENTER R3700

AMENDMENT UNDER 37 C.F.R. §1.111

Assistant Commissioner for Patents  
Washington, D.C. 20231

Date: December 11, 2002

Sir:

This Amendment responds to the Office Action dated August 12, 2002 (Paper No.

6). Concurrently filed with this Amendment is a Petition for Extension of Time for one month. Please amend above-noted application as follows:

IN THE CLAIMS:

Please add new claim 18 as follows:

18. (New) A method for manufacturing electronic devices obtained by at least maintaining electronic component chips on a printed circuit board, comprising the steps of:  
supplying a plurality of electronic component chips in an aligned relationship,  
wherein aligned electronic component chips are taken out one at a time from a chute portion connected to a buffer portion; and  
cleaning external electrodes of the electronic component chips.

C1